

Tobias Haelke

educational work

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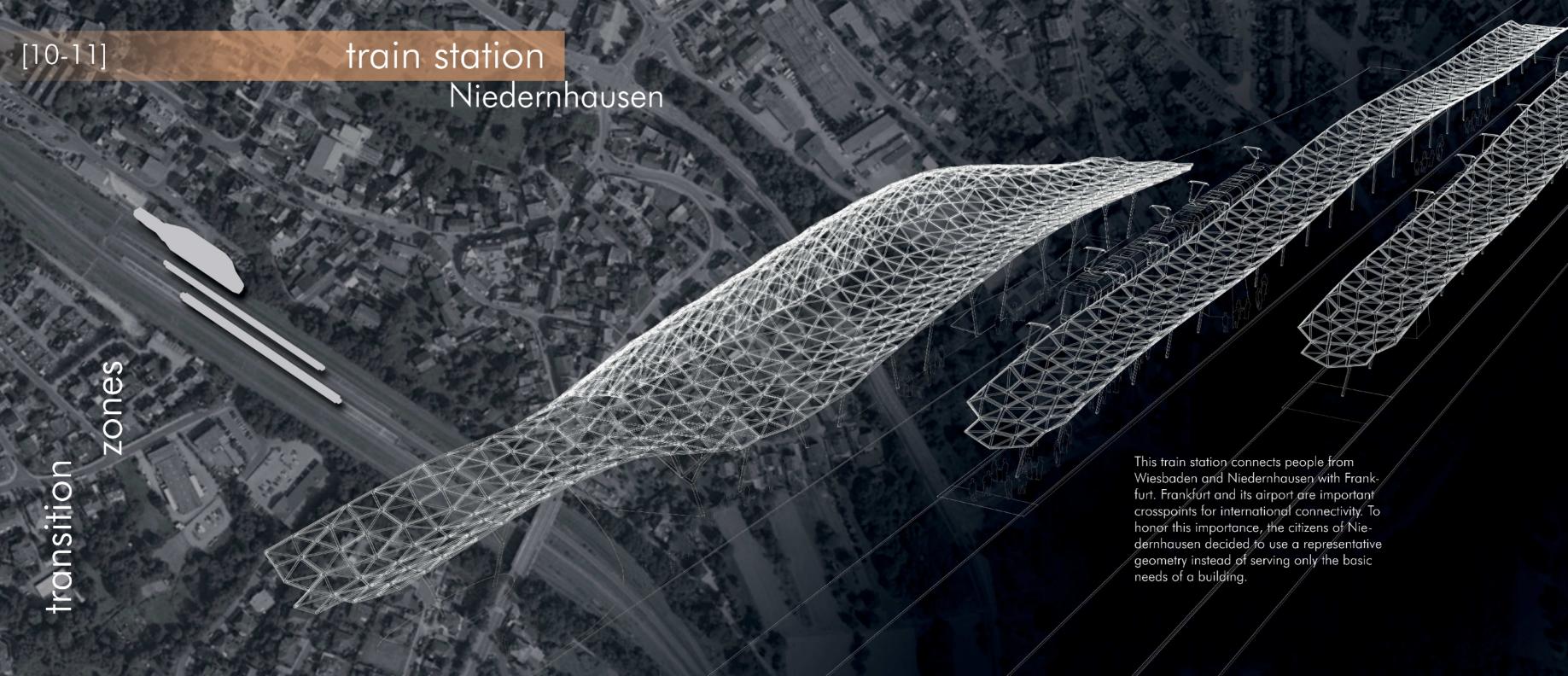
curriculum vitae

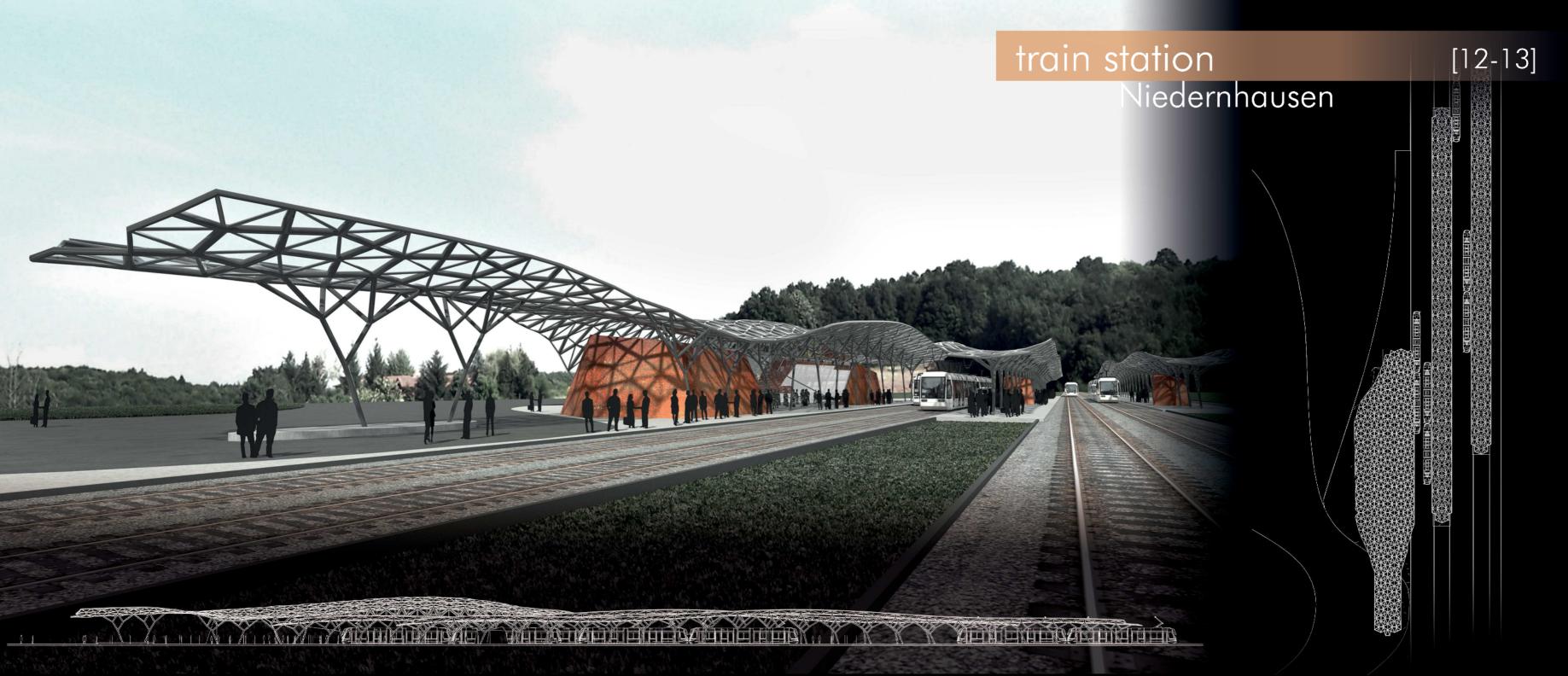
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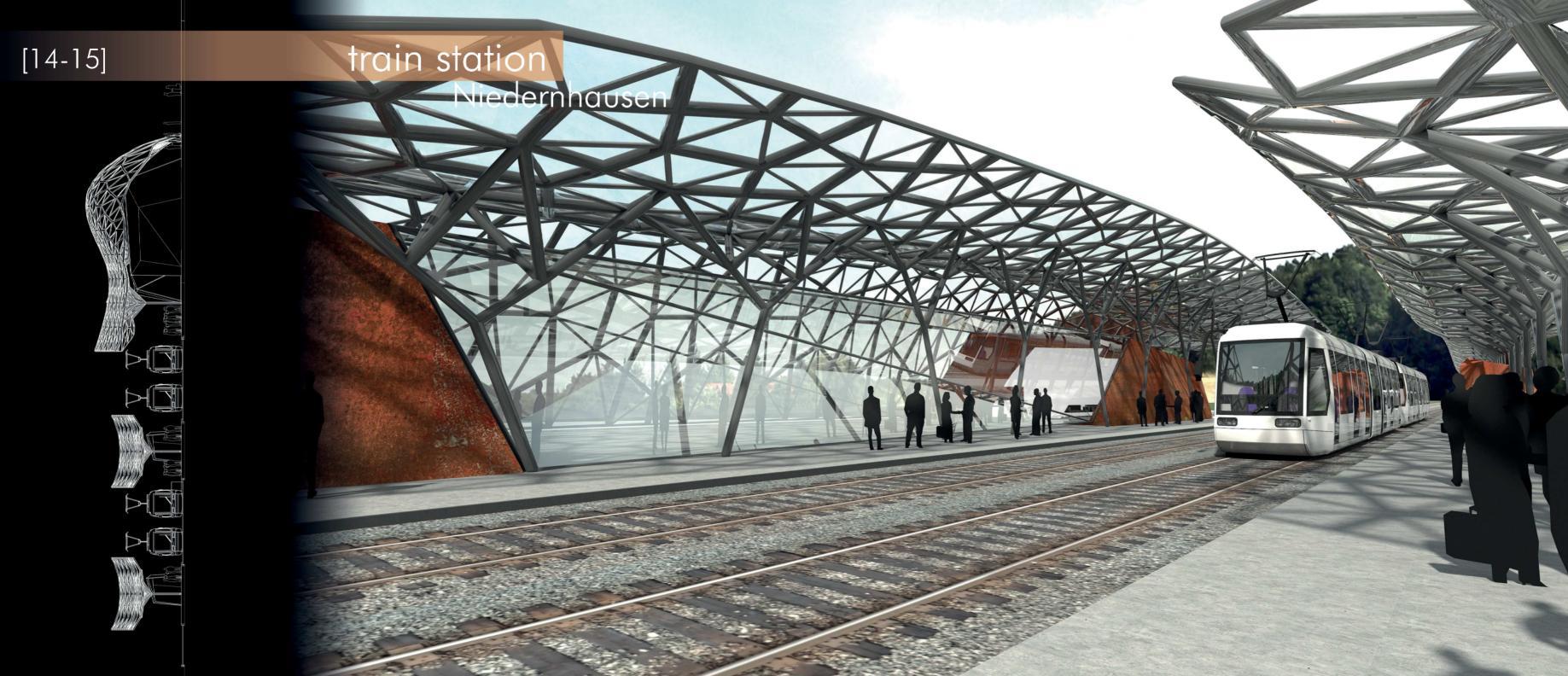






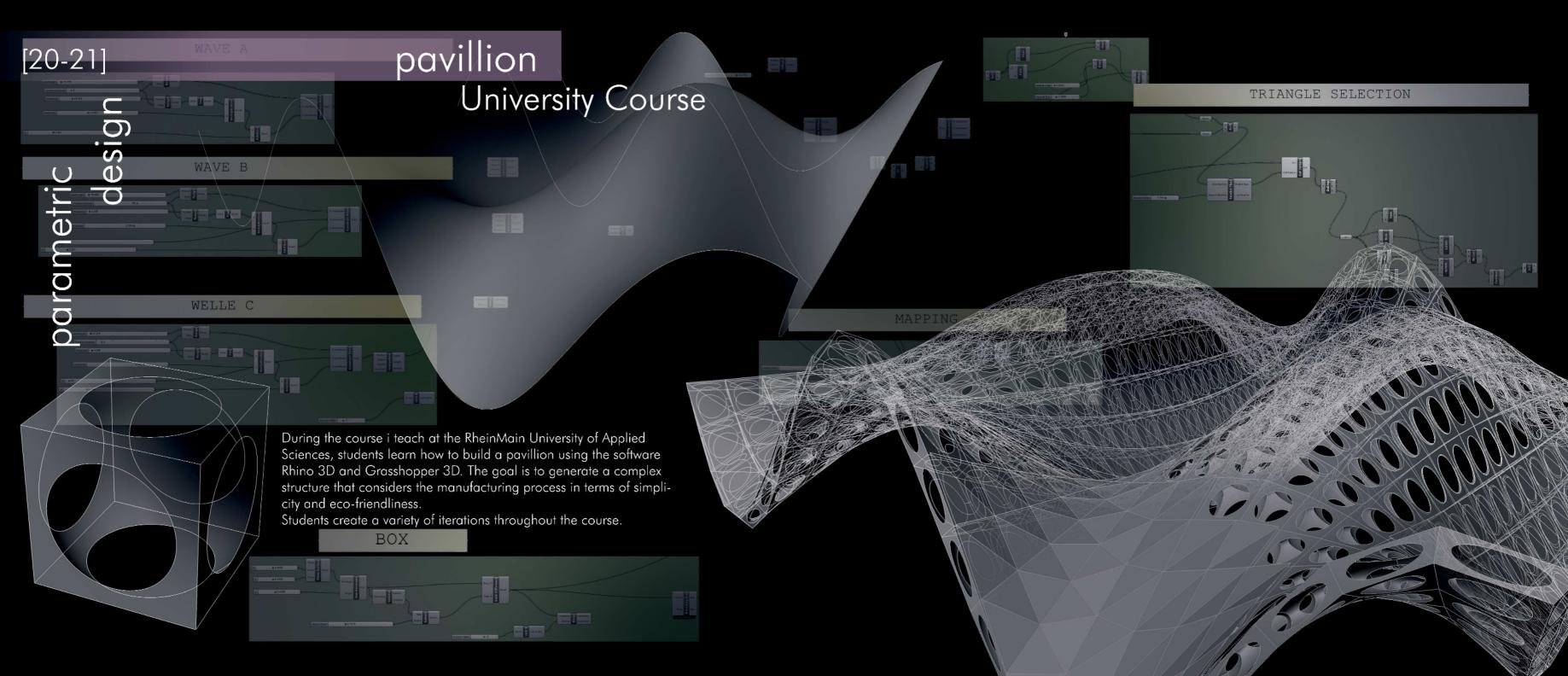




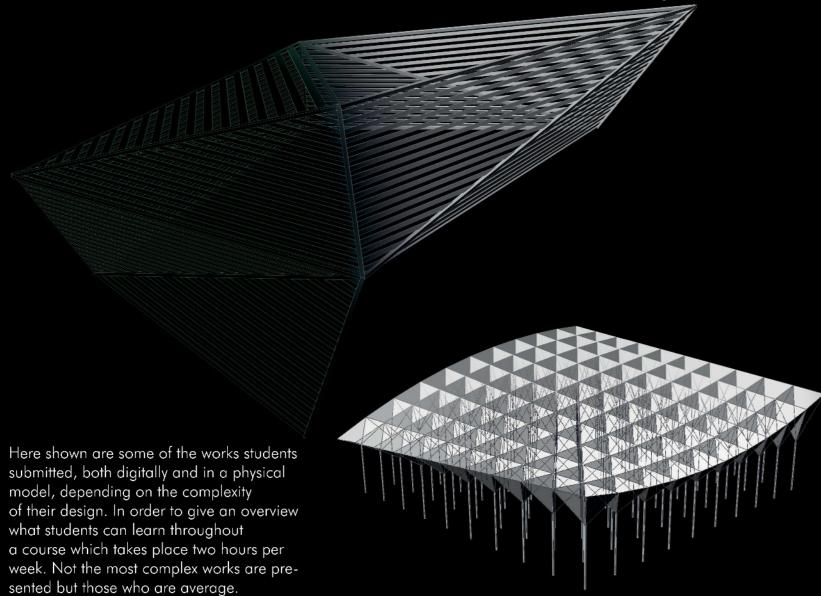


professional work





pavillion University Course



curriculum vitae

Personal Information		
	Name	Tobias Haelke
	Born	09/10/1988 in Wiesbaden
	Adress	Reichsapfelstraße 15
		65201 Wiesbaden
	Telephone	0611 – 26 11 36
		01575 – 2001636
	E-Mail	tobias@haelke.com
	Nationality	German
	Religious denomination	Roman Catholic
	Family status	Single
Education	,	3
	08/1995-07/1999	Hafenschule Wiesbaden-Schierstein (primary school)
	08/1999-07/2003	Gymnasium Am Mosbacher Berg (secondary school)
	08/2003-07/2006	Werner-von-Siemens-Schule mit Realschulabschluss
		(secondary school certificate)
		(leadership of the music working group)
	08/2006-06/2009	Friedrich-List-Schule (secondary school, subject area: Data processing)
Graduation		University-Entrance Diploma (06/2009)
	01/08/2009-30/04/2010	Alternative civilian service: St. Josefs-Hospital Wiesbaden
	01/05/2010-30/09/2010	Employee: St. Josefs-Hospital Wiesbaden.
Studies		
	01/10/2009-30/09/2011	Two semesters psychology at FU Hagen
	01/09/2011	Start of architectural studies: RheinMain UoAS
	17/02/2014-24/06/2014	Semester abroad: QUT (Brisbane, QLD, Australia)
	01/04/2015	Student assistant for parametric design, maintenance of PC-pools,
		maintenance of technical equipment in administration, evaluation
	21/09/2015	B.A. Architecture at RheinMain University of Applied Sciences
	01/10/2015	Lectureship for parametric design
Work Experience		
	07/2011-09/2011	Internship at Elmar Krebber Architekten
	08/2013-02/2014	Cooperation with BLOCK 3
	11/2015-01/2016	Internship at Willen Associates
Computer Literacy		
'		Rhino 3D, Grasshopper 3D, AutoCAD, Photoshop,
		Illustrator, InDesign, Word, Excel, Java, Arduino, Processing etc.